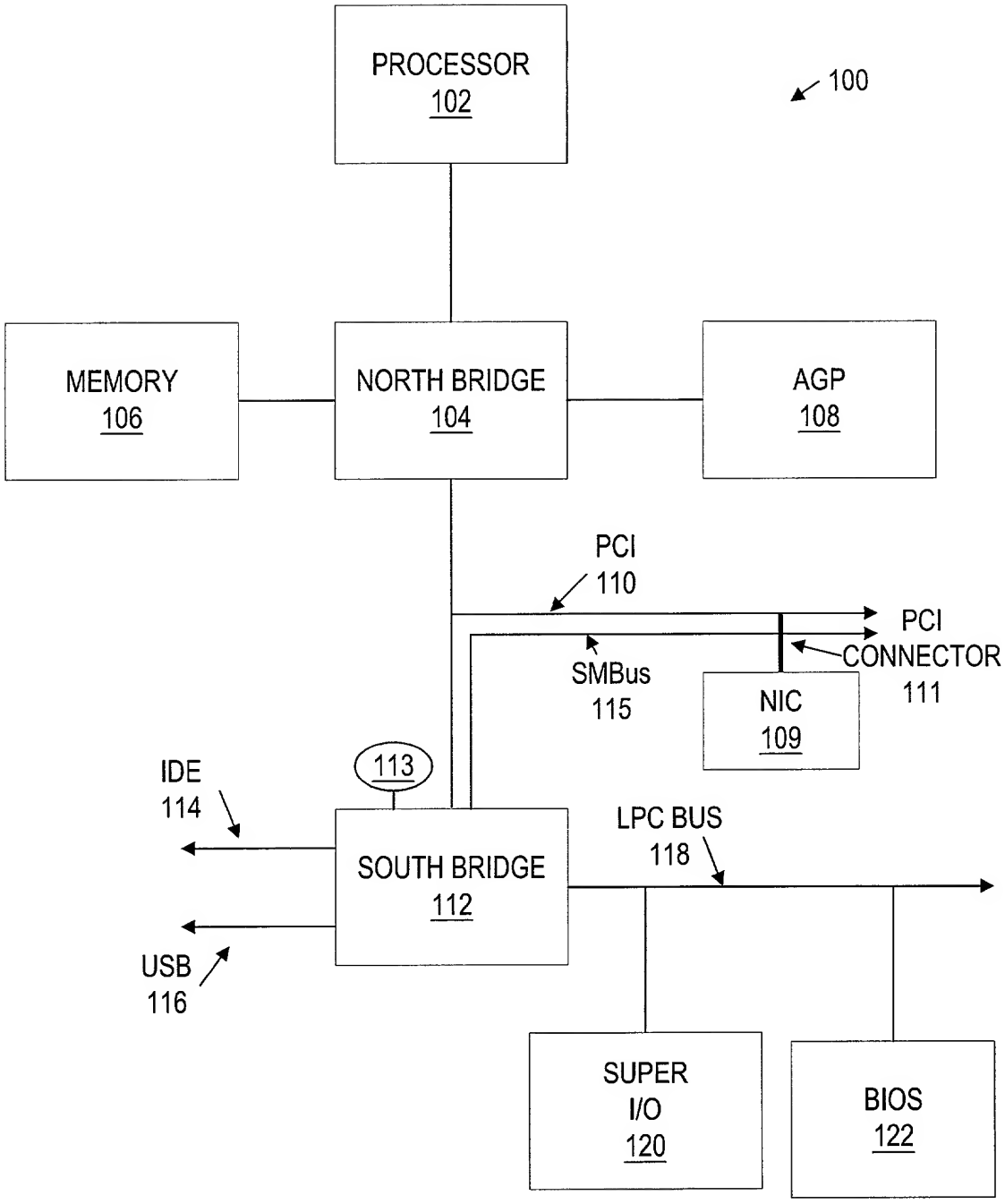

Fig. 1A
(Prior Art)

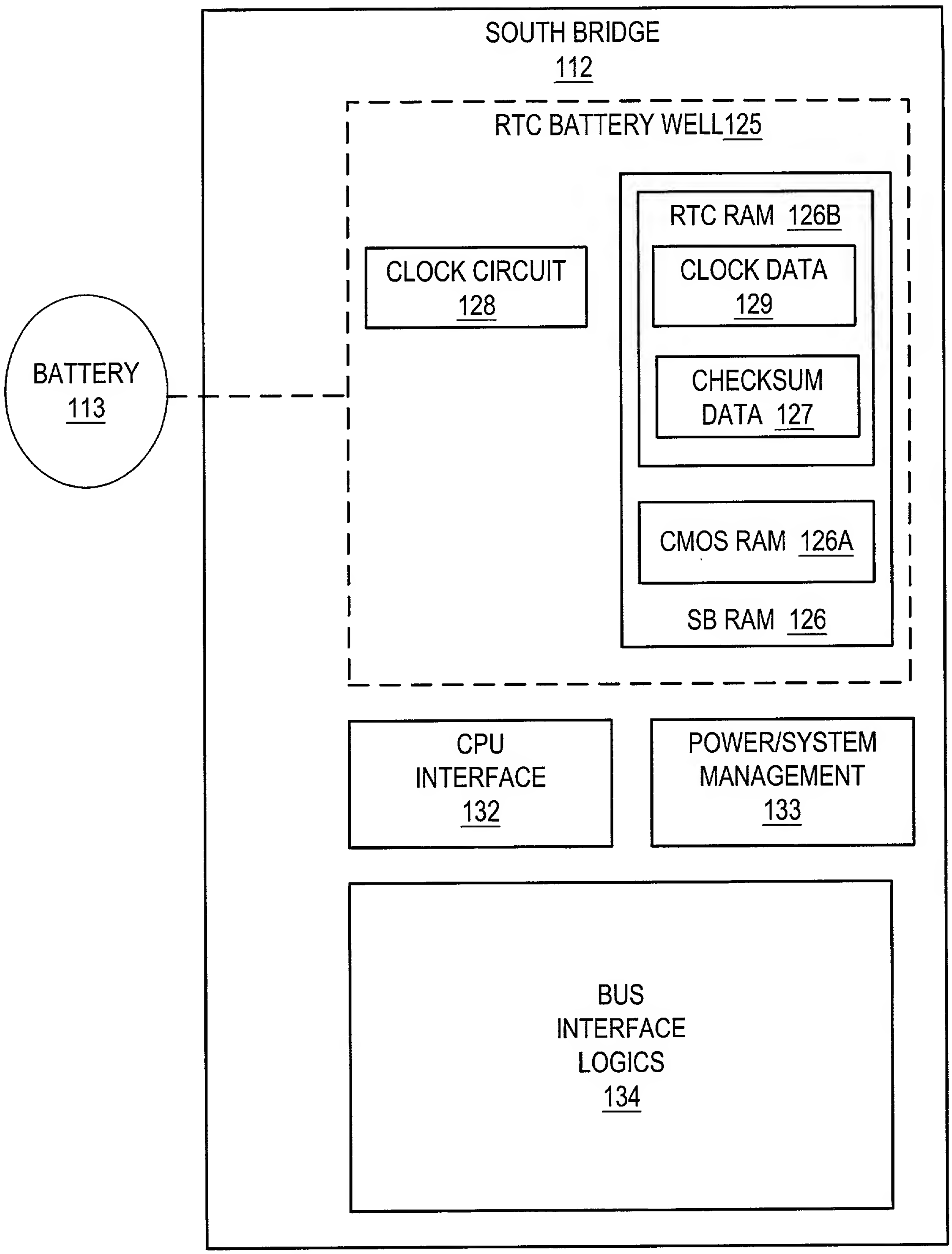

Fig. 1B
(Prior Art)

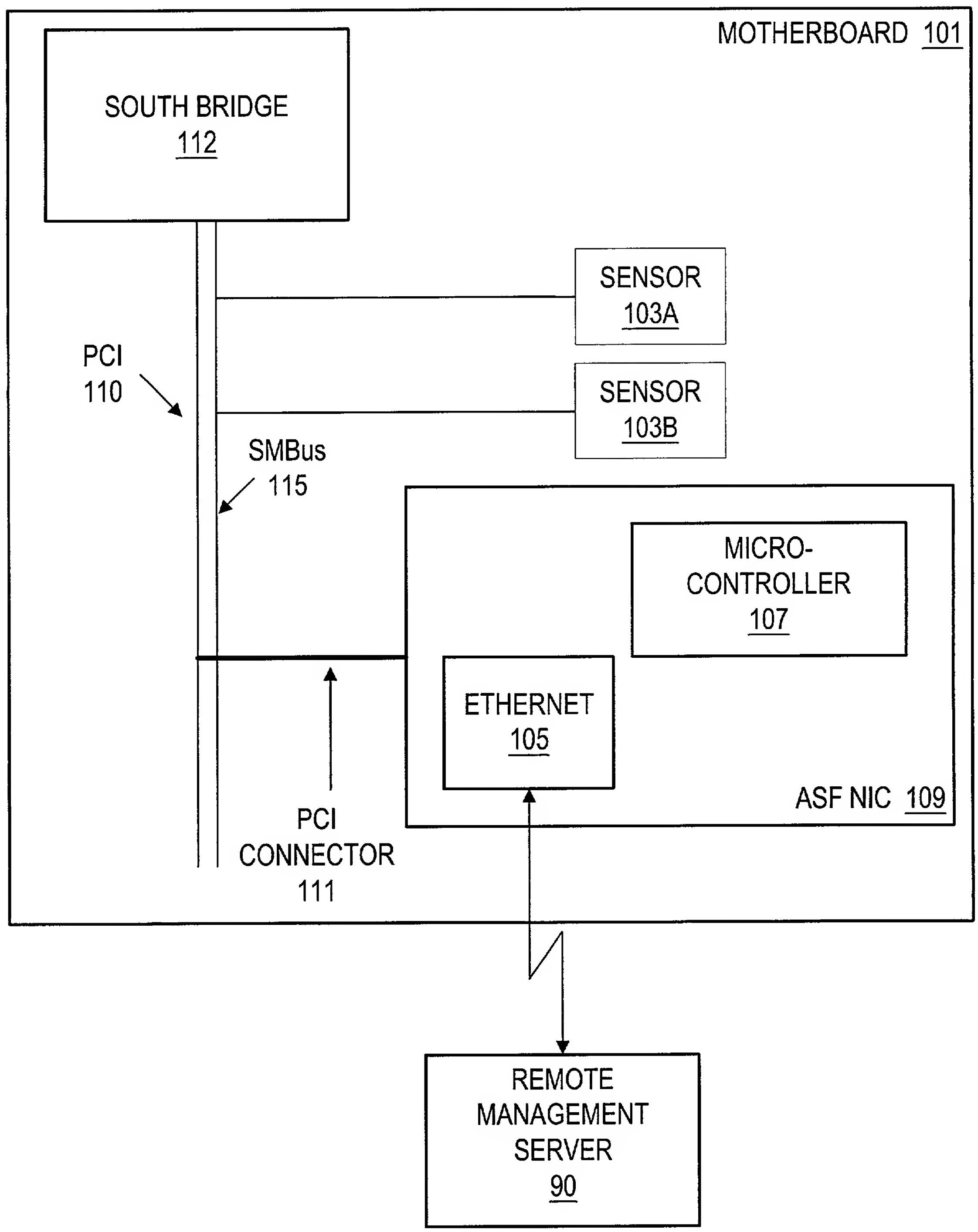

Fig. 1C
(Prior Art)

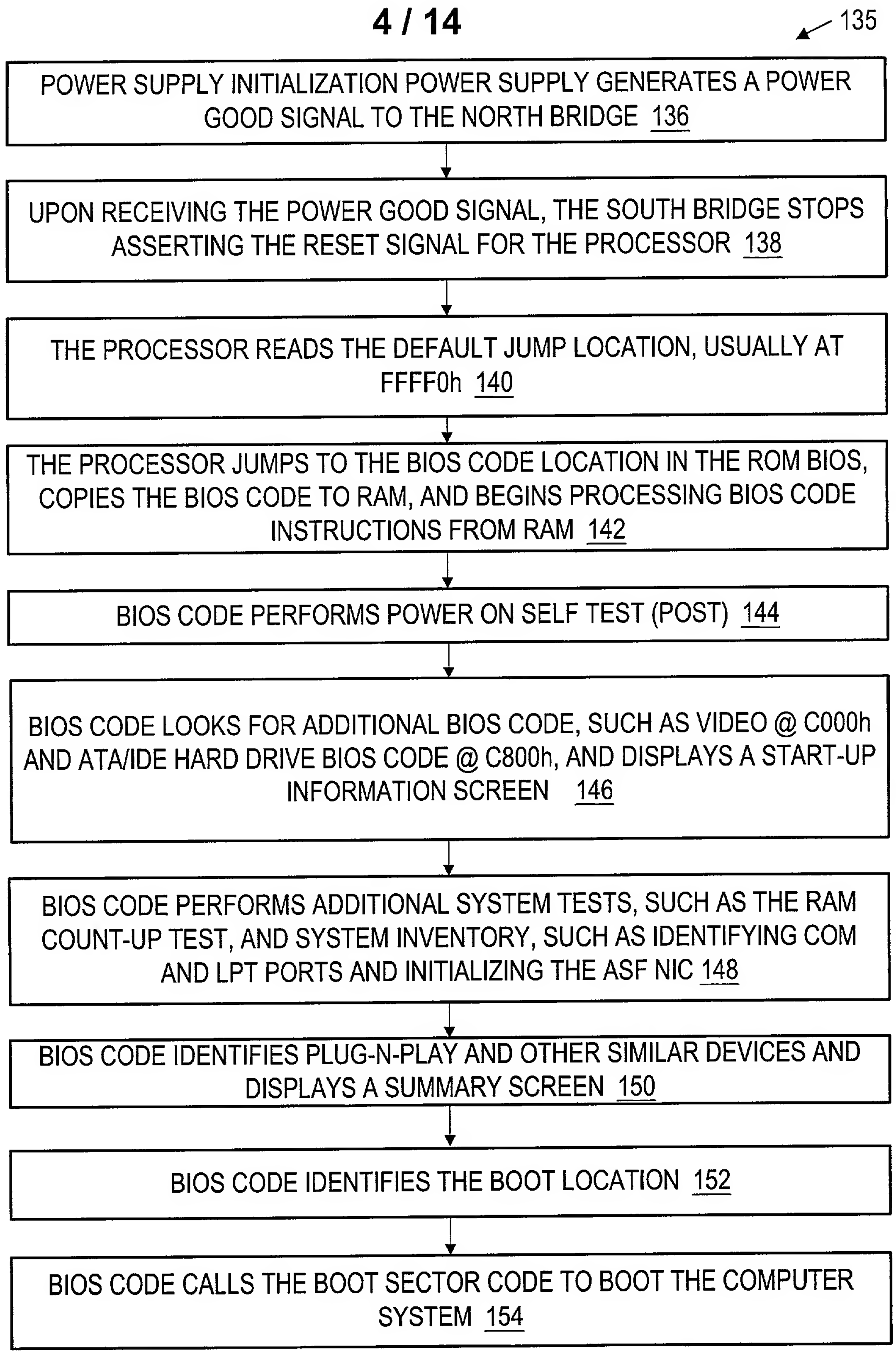

Fig. 2
(Prior Art)

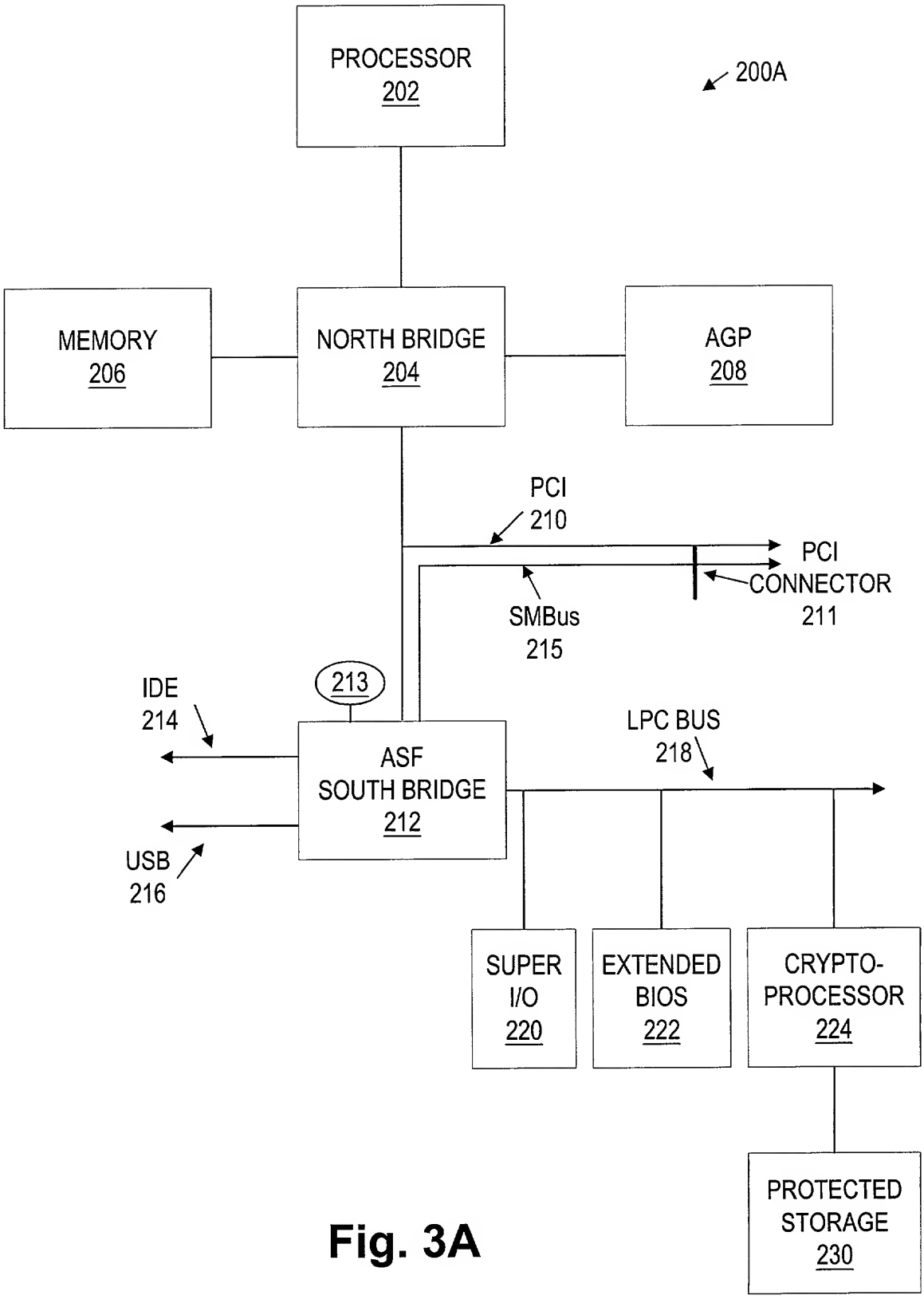
PROCESSOR
202
200A
MEMORY
206
NORTH BRIDGE
204
AGP
208
PCI
210
PCI
CONNECTOR
211
SMBus
215
213
IDE
214
ASF
SOUTH BRIDGE
212
LPC BUS
218
USB
216
SUPER
I/O
220
EXTENDED
BIOS
222
CRYPTO-
PROCESSOR
224
PROTECTED
STORAGE
230

Fig. 3A

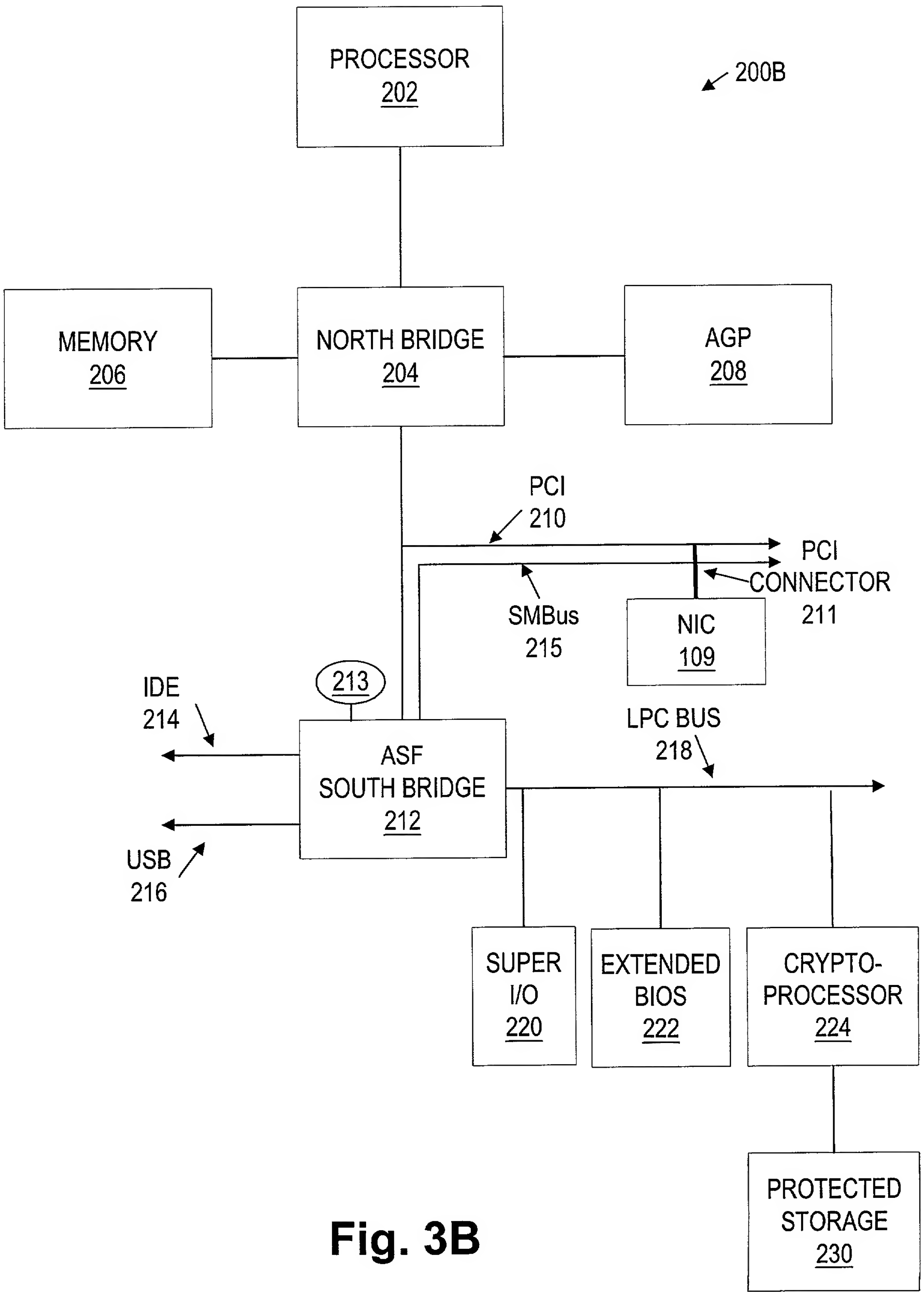

Fig. 3B

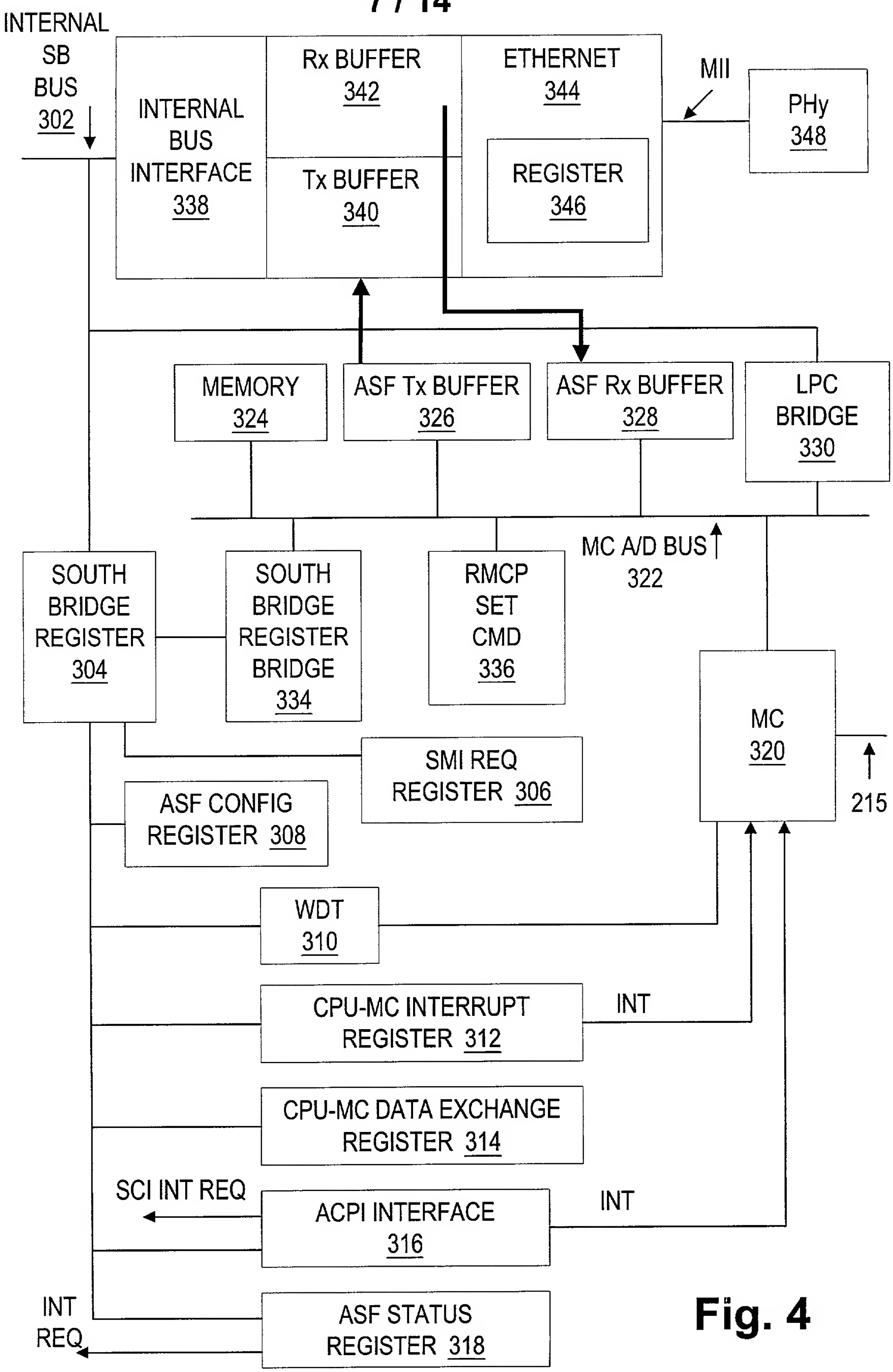

Fig. 4

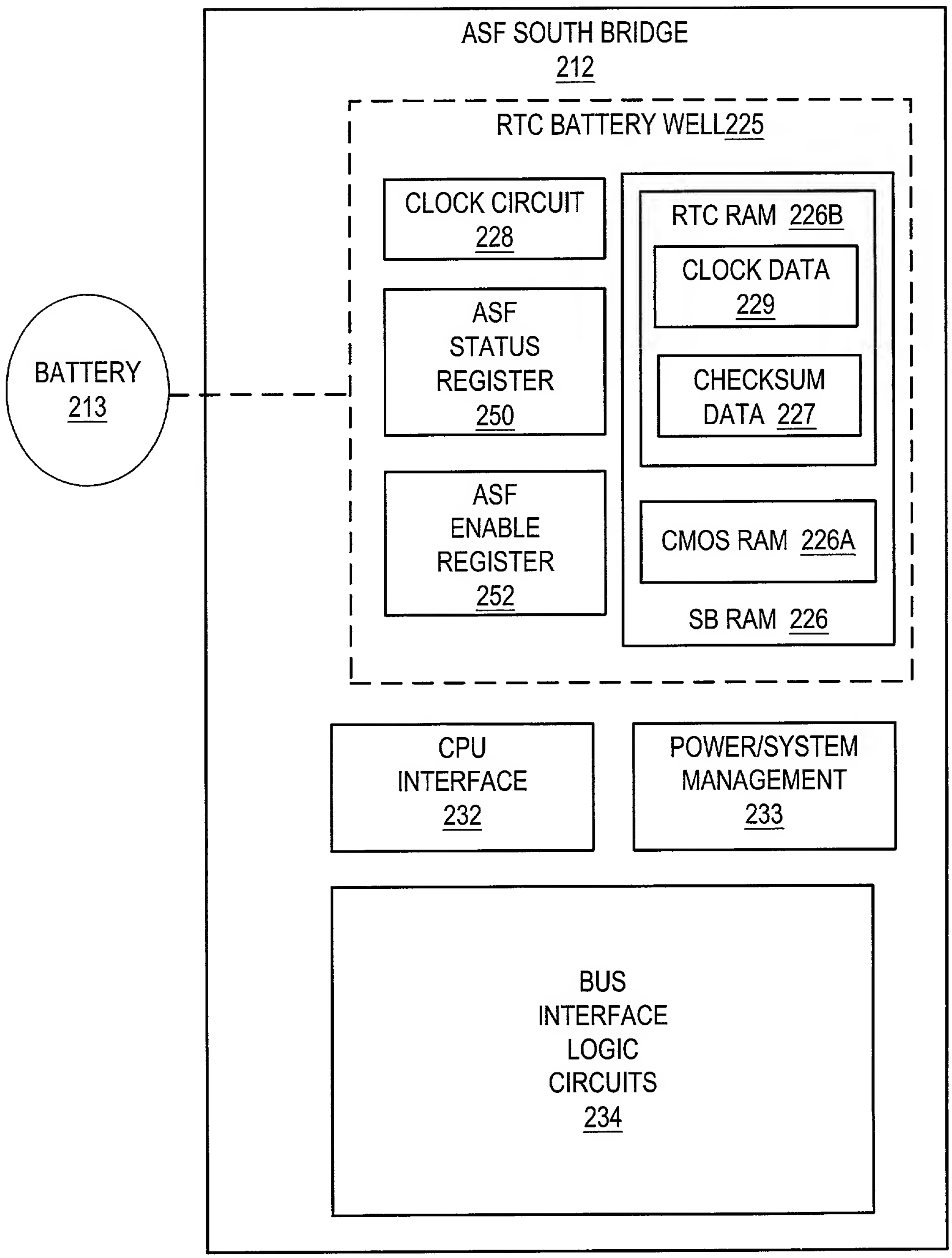

Fig. 5

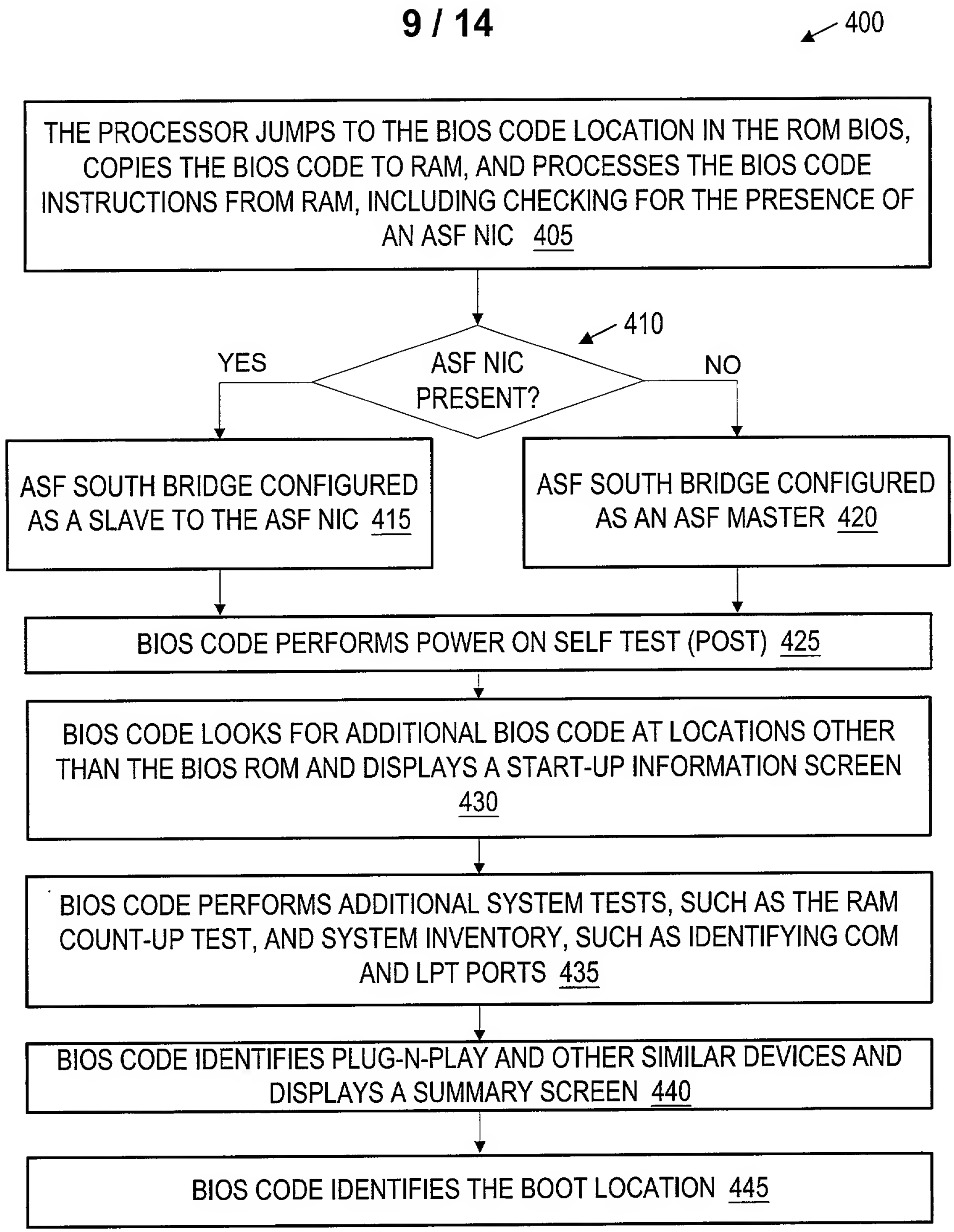
400
THE PROCESSOR JUMPS TO THE BIOS CODE LOCATION IN THE ROM BIOS, COPIES THE BIOS CODE TO RAM, AND PROCESSES THE BIOS CODE INSTRUCTIONS FROM RAM, INCLUDING CHECKING FOR THE PRESENCE OF AN ASF NIC 405
410
ASF NIC PRESENT?
YES
NO
ASF SOUTH BRIDGE CONFIGURED AS A SLAVE TO THE ASF NIC 415
ASF SOUTH BRIDGE CONFIGURED AS AN ASF MASTER 420
BIOS CODE PERFORMS POWER ON SELF TEST (POST) 425
BIOS CODE LOOKS FOR ADDITIONAL BIOS CODE AT LOCATIONS OTHER THAN THE BIOS ROM AND DISPLAYS A START-UP INFORMATION SCREEN 430
BIOS CODE PERFORMS ADDITIONAL SYSTEM TESTS, SUCH AS THE RAM COUNT-UP TEST, AND SYSTEM INVENTORY, SUCH AS IDENTIFYING COM AND LPT PORTS 435
BIOS CODE IDENTIFIES PLUG-N-PLAY AND OTHER SIMILAR DEVICES AND DISPLAYS A SUMMARY SCREEN 440
BIOS CODE IDENTIFIES THE BOOT LOCATION 445

Fig. 6

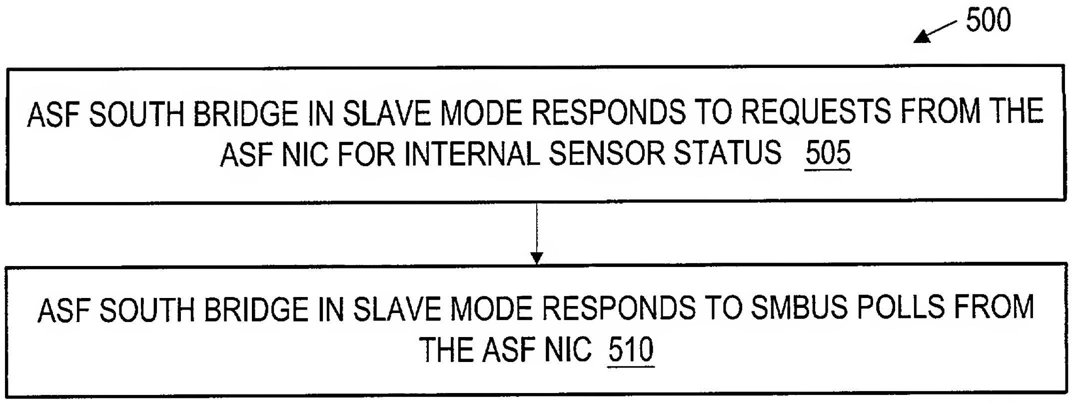

Fig. 7A

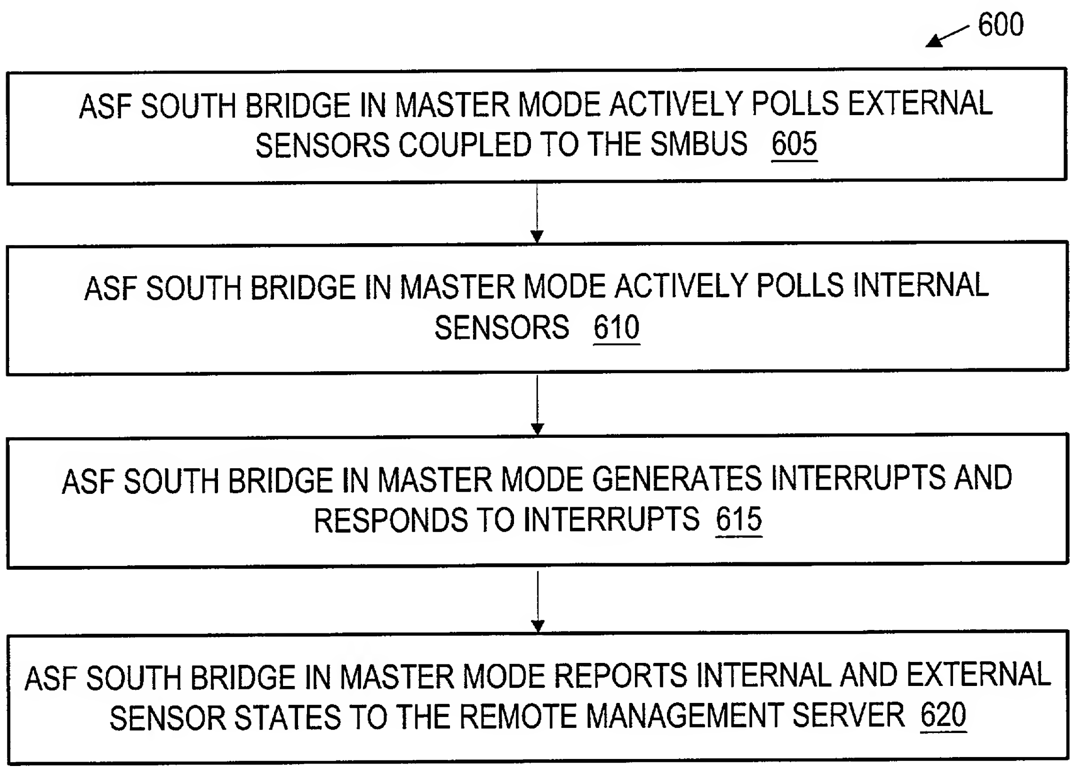

Fig. 7B

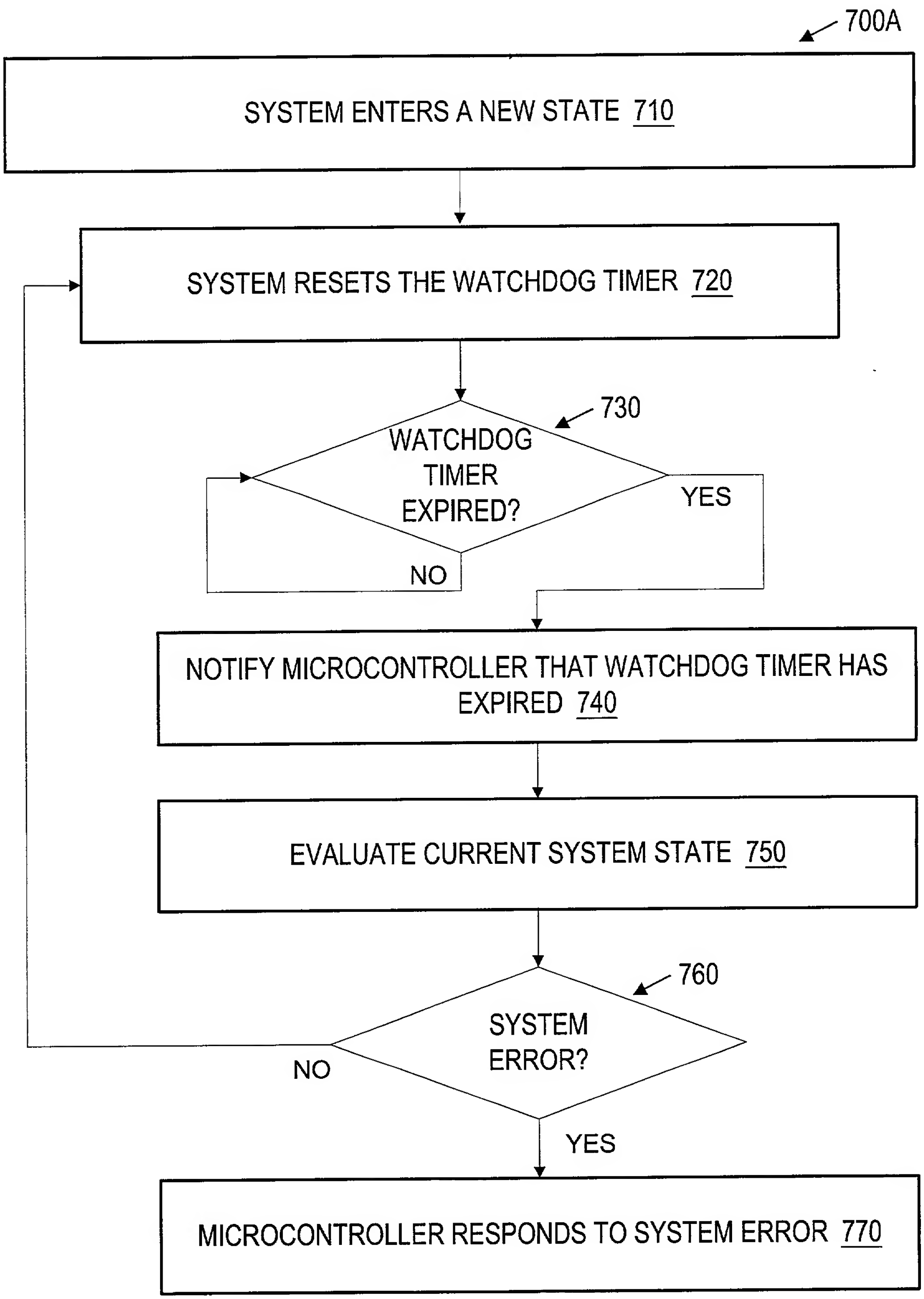
700A
SYSTEM ENTERS A NEW STATE 710
SYSTEM RESETS THE WATCHDOG TIMER 720
730
WATCHDOG TIMER EXPIRED?
YES
NO
NOTIFY MICROCONTROLLER THAT WATCHDOG TIMER HAS EXPIRED 740
EVALUATE CURRENT SYSTEM STATE 750
760
SYSTEM ERROR?
NO
YES
MICROCONTROLLER RESPONDS TO SYSTEM ERROR 770

Fig. 8A

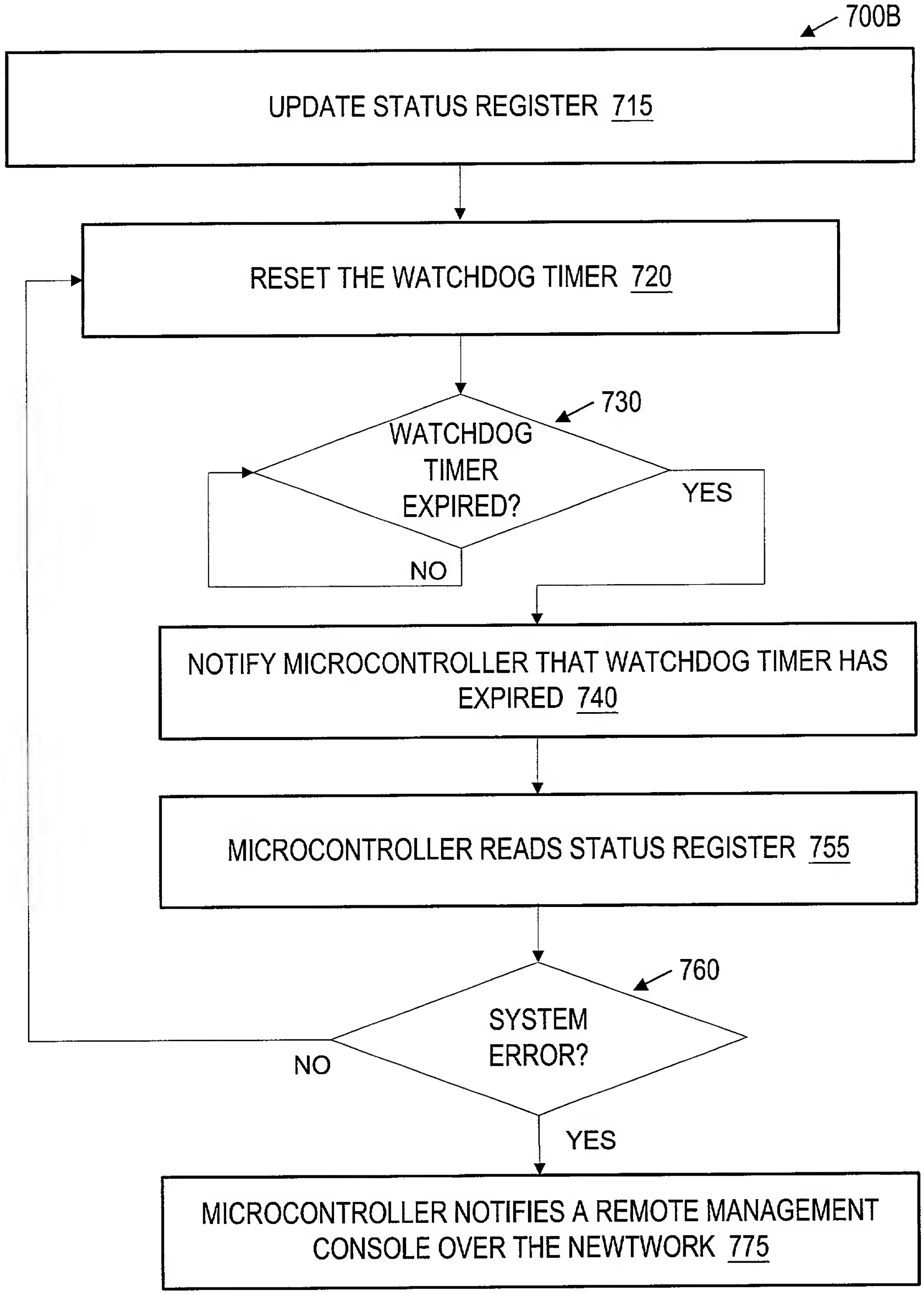

Fig. 8B

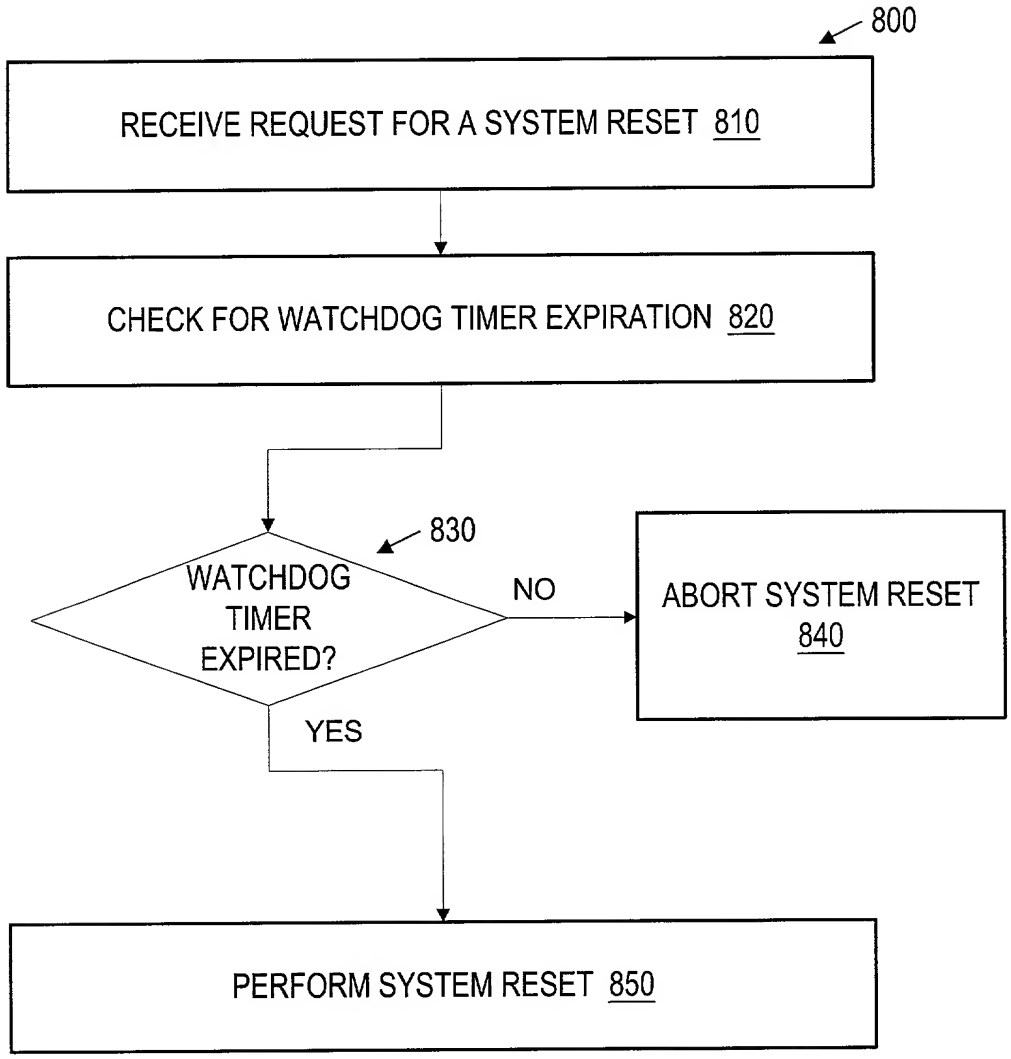

Fig. 9

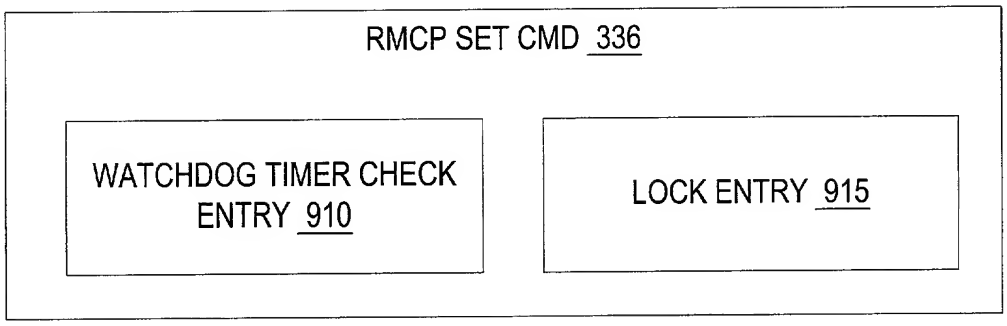

Fig. 10

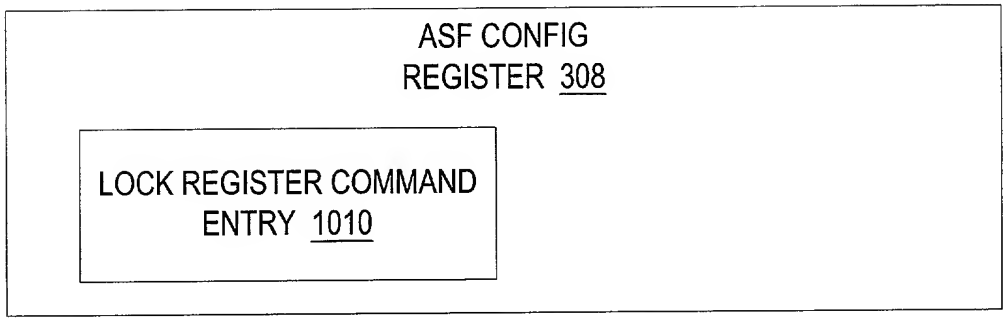

Fig. 11